

# SPROG 3 DCC Decoder Programmer User Guide Supplement



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### Introduction

SPROG 3 is a DCC decoder programmer and command station interface for connection to the USB port of a personal computer or similar device. SPROG 3 is supported by the free JMRI software (<u>http://jmri.sourceforge.net/</u>) and http://wiki.rocrail.net/doku.phpRocrail also free.

SPROG 3 is also capable of operating a medium size layout, supplying up to 2.5 Amps. An even larger layout can be driven by using an external booster.

# Read this document in conjunction with the SPROG IIv3 User Guide available on the CD-ROM and also on the SPROG DCC website.

This Supplement covers SPROG 3 specific information only:

- Hardware specification
- USB driver installation.
- Track output current limit
- Firmware updates (bootloader)

#### **Requirements**

- JMRI from <a href="http://jmri.sourceforge.net/">http://jmri.sourceforge.net/</a> or the supplied CD-ROM
- Regulated DC Power Supply (see Table 1)
- Short length or small oval of track for programming and/or test running
- Rocrail, optional alternative to JMRI

#### **Features**

- Booster stage supplying up to 2.5 Amp to track
- Programs virtually **all** NMRA compliant DCC decoders
- No extra hardware required for programming sound decoders (e.g. QSI, Soundtraxx)
- Easy to use graphical interface with DecoderPro
- USB interface for easy connection to PC
- USB activity LED shows communication with the PC
- Power LED flashes when programming track power is live

#### Specification/Operating Conditions

Parameter	Minimum	Nominal	Maximum	Units
DC Input supply	10V		20V	V
voltage	Note 1			
Vin supply current –		50		mA
not programming				
Vin supply current –		300		mA
programming		Note 2		
Vin supply current –			2.6	А
Operating Layout			Note 3	
Operating		25		°C
Temperature Range				
Output Load -			250	mA
programming			Note 2	
Output Load –			2.5	А
Operating Layout			Note 3	

#### Notes:

- 1. Minimum supply voltage depends upon the requirements of the decoder being programmed. In general it is safer to use as low a voltage as possible in case of problems with a newly installed decoder.
- 2. SPROG 3 will remove track power if output current exceeds 250mA as measured 100ms after applying power. Surge current during decoder power-up may be considerably greater than this.
- 3. To ensure correct operation of the SPROG 3 current limit, the power supply must be capable of sustaining greater then 2.5 Amp.
- 4. SPROG 3 is protected against reverse polarity connection of the power supply but will not work unless the polarity is correct.

#### Table 1 Specification/Operating Conditions

SPROG 3 is protected against reverse polarity connection of the power supply, but will not work unless the polarity is correct.

#### SPROG 3 is <u>not</u> protected against track and power connections being interchanged.

# Installation

#### Install SPROG 3 USB drivers

Follow the instructions in the SPROG IIv3 User Guide but when prompted to browse to the driver directory on the CD-ROM browse to the sprog3\inf directory. For example, if your CD-ROM drive is D: browse to D:\USB\sprog3\inf as shown:

	Mute Lock Power Plan Sle
$\bigcirc$	Update Driver Software - SPROG 3 DCC Programmer
	Browse for driver software on your computer
	Search for driver software in this location:
	D:\USB\sprog3\inf
	✓ Include subfolders
	Let me pick from a list of device drivers on my computer This list will show installed driver software compatible with the device, and all driver software in the same category as the device.
	Next Cancel

# **The SPROG Console**

Operation of the SPROG console is as described in the SPROG II User Guide with the exception of the Current Limit setting.

#### **Current Limit**

Set the current limit for the SPROG 3 track output when using a SPROG 3 throttle, or when connected in Command Station mode. The maximum current limit is 2499mA or 2.499 Amps.

# **Updates to the SPROG 3 Firmware**

#### Firmware Update Using the Bootloader

Follow the instructions in the SPROG II User Guide, selecting SPROG 3 Firmware Update rather than SPROG II Firmware Update.